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PATENT APPLICATION

ATTORNEY DOCKET NO. 10016598-1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Terry Ping-Chung Lee

Confirmation No.:

Application No.: 09/991453

Examiner:

Filing Date: 11/14/2001

Group Art Unit:

Title: METHOD OF ERROR ISOLATION FOR SHARED PCI SLOTS

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

RECEIVED
JAN 15 2002
Technology Center 2100

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
() No additional fee (Address envelope to "Box Non-Fee Amendments")
(X) Other: 2 return postcards (fee \$ _____)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS		MINUS		= 0	X \$18	\$ 0
INDEP. CLAIMS		MINUS		= 0	X \$84	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$280	\$ 0
EXTENSION FEE	1ST MONTH \$110.00	2ND MONTH \$400.00	3RD MONTH \$920.00	4TH MONTH \$1440.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Respectfully submitted,

Terry Ping-Chung Lee

By R. A. Fuller III

Roland A. Fuller III

Attorney/Agent for Applicant(s)

Reg. No. 31,160

Date: 12/07/2001

Date of Deposit: 12/07/2001

Typed Name: Theresa N. Powless

Signature: Theresa N. Powless

#3/A
1-25-02
DW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application No.: 09/991453
Filing Date: November 14, 2001
Applicant: Terry Ping-Chung Lee
Group Art Unit: [Unknown]
Examiner: [Unknown]
Title: METHOD OF ERROR ISOLATION FOR SHARED PCI SLOTS
Attorney Docket: 10016598-1

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Commissioner of Patents and Trademarks
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to the examination of this application, please amend it as follows:

IN THE SPECIFICATION

Please replace the following paragraphs of the specification. Applicant includes herewith an Attachment for Specification Amendments showing a marked up version of each replacement paragraph.

Please replace Paragraph [0022] with the following paragraph:

[0022] Turning to Figure 4, the PCI error routine is described. When a PCI error occurs, firmware first invalidates at 80 all map entries in the firmware maintained PCI resource allocation map for PCI slots having their hot plug flags set. At 81, the hot plug flags are cleared. Firmware next checks at 82 to see if the host bridge, such as host bridge 203 in Figure 6, logged an error address. If not,

A1